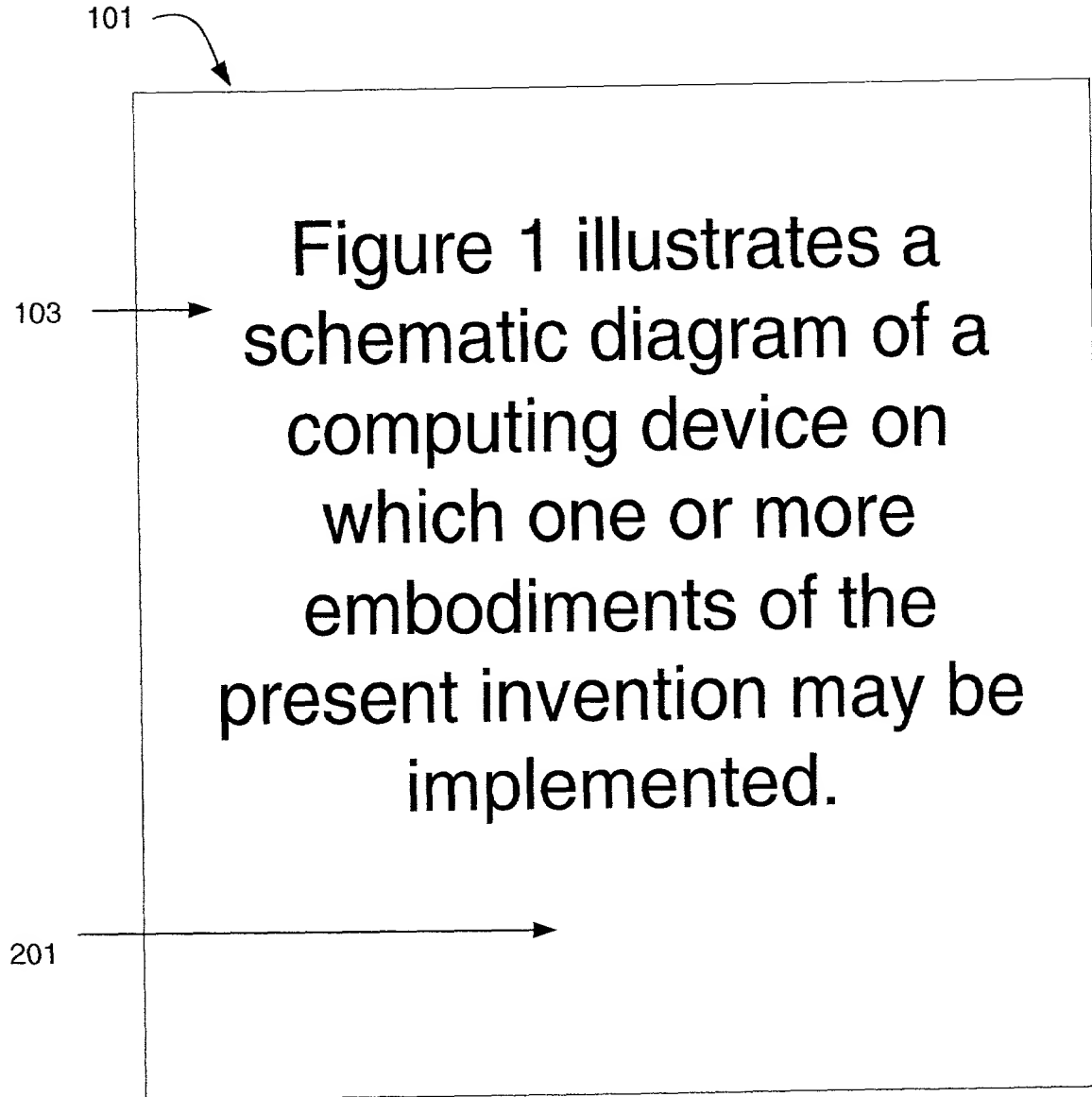


FIG. 1
(Prior Art)



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DOCKET No.: 03797.00135

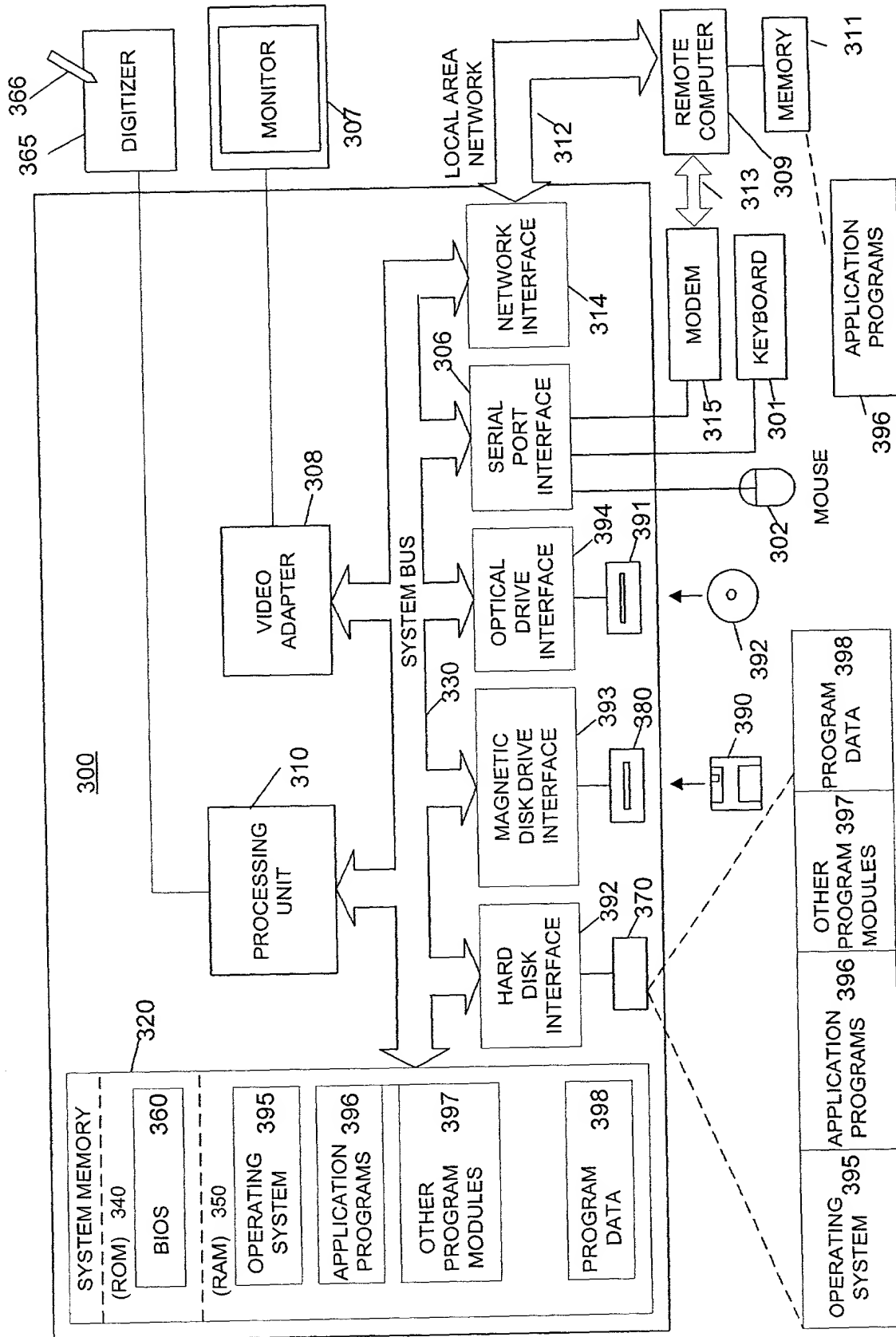


FIG. 3

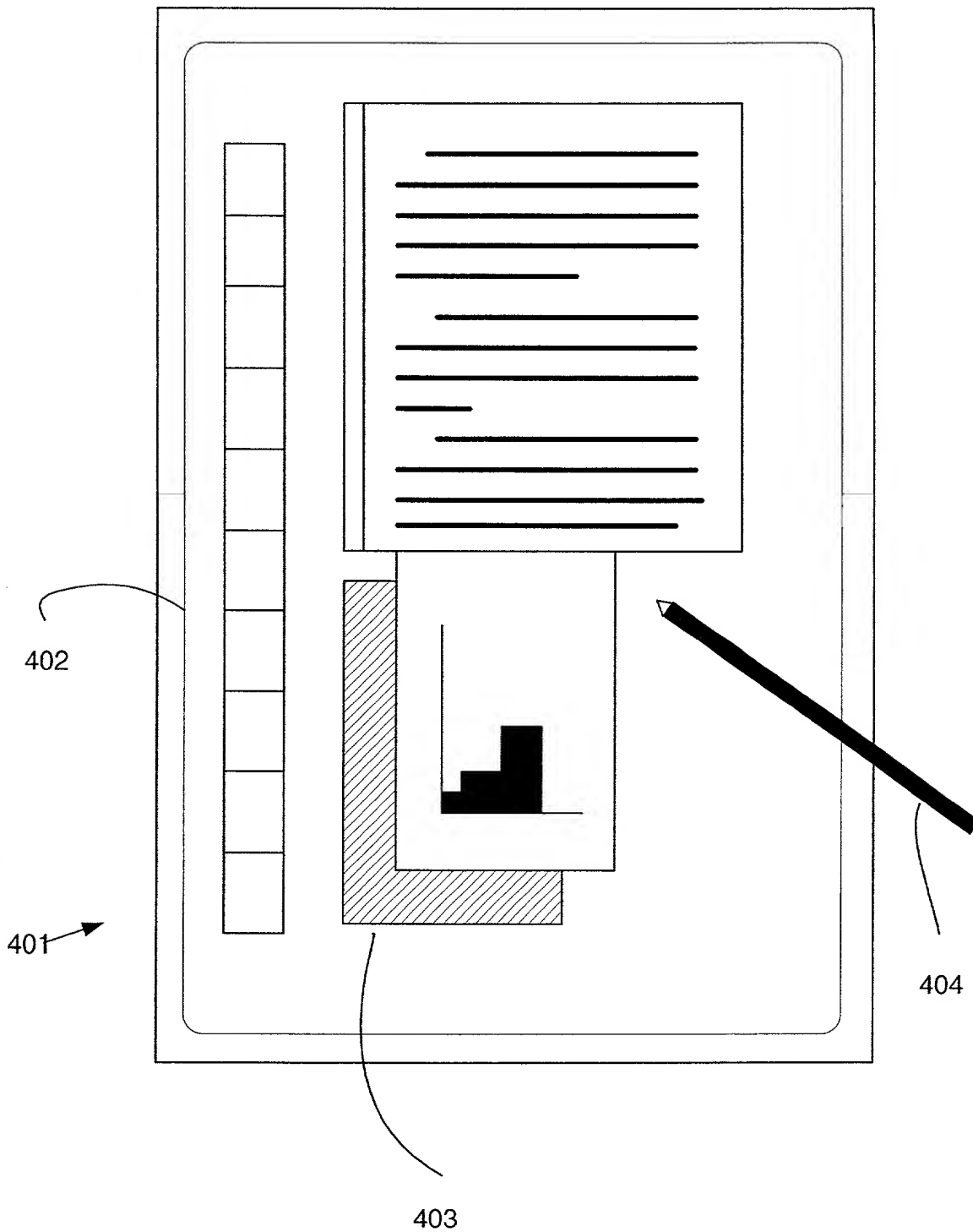


FIG. 4

501

505

Figure 1 illustrates a schematic
diagram of a computing device
on which one or more
embodiments of the present
invention may be implemented.

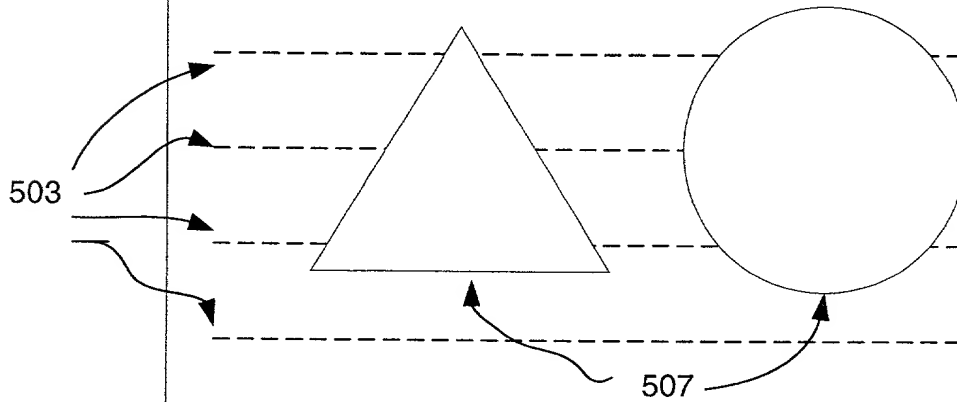


FIG. 5

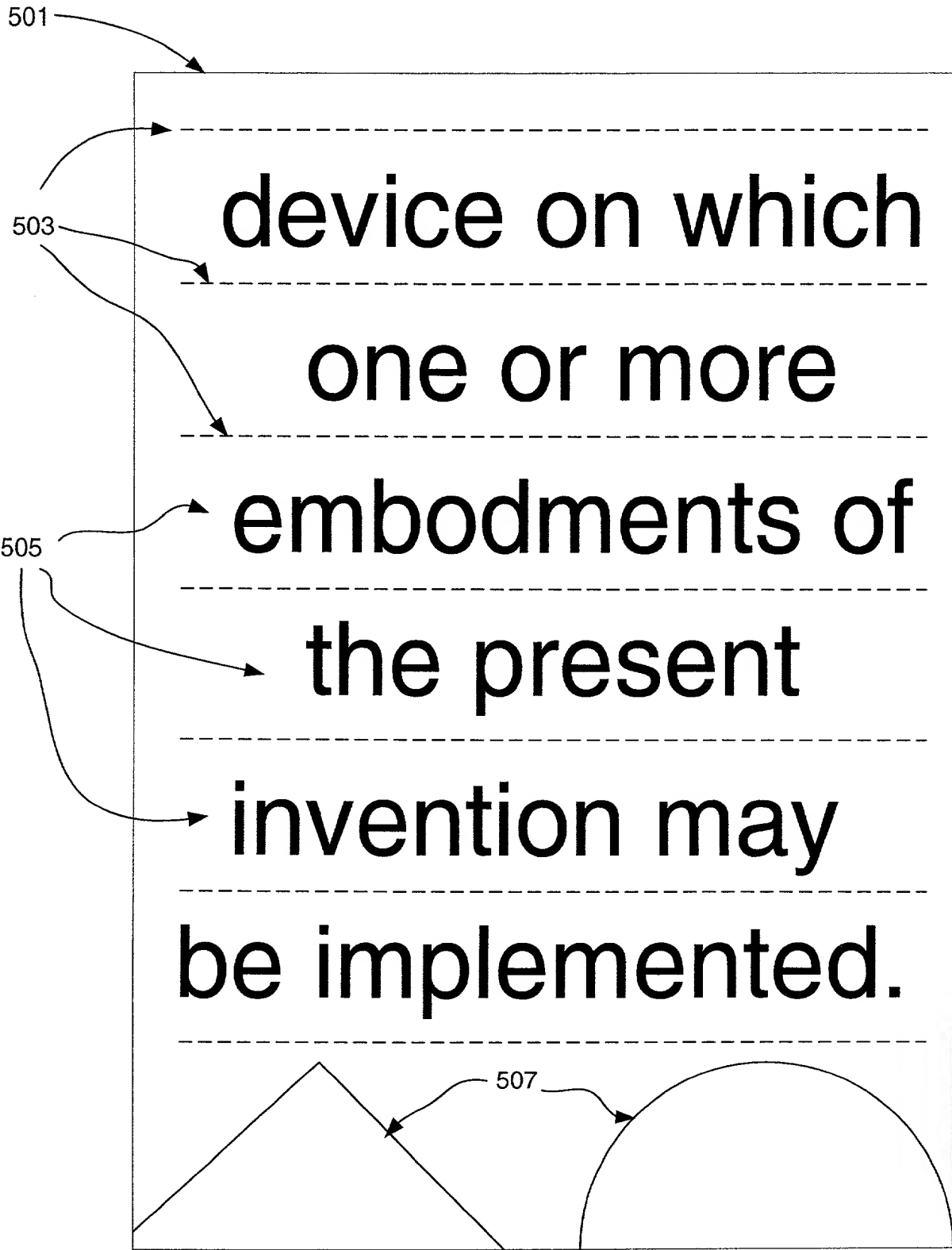


FIG. 6

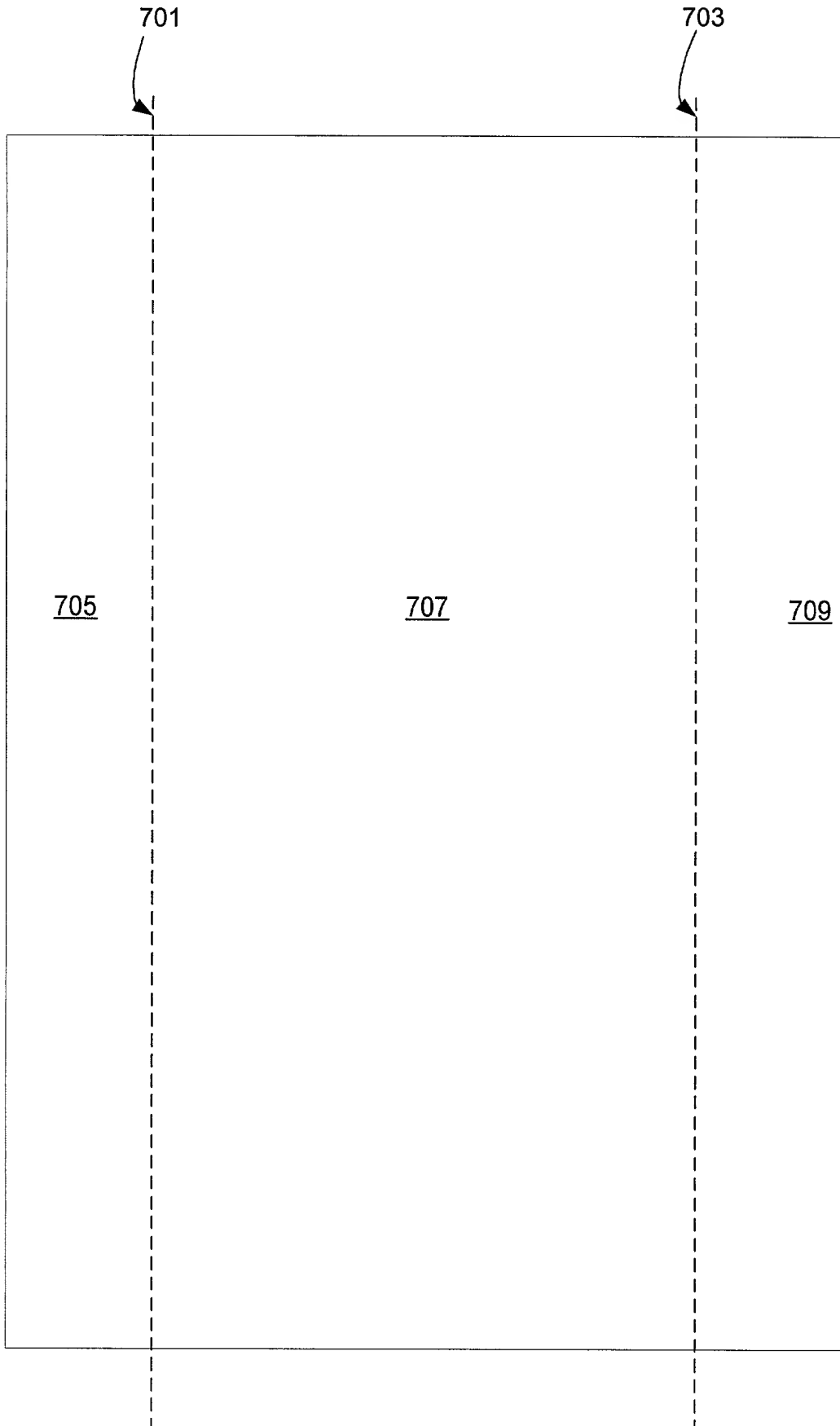


FIG. 7

09918722-080404

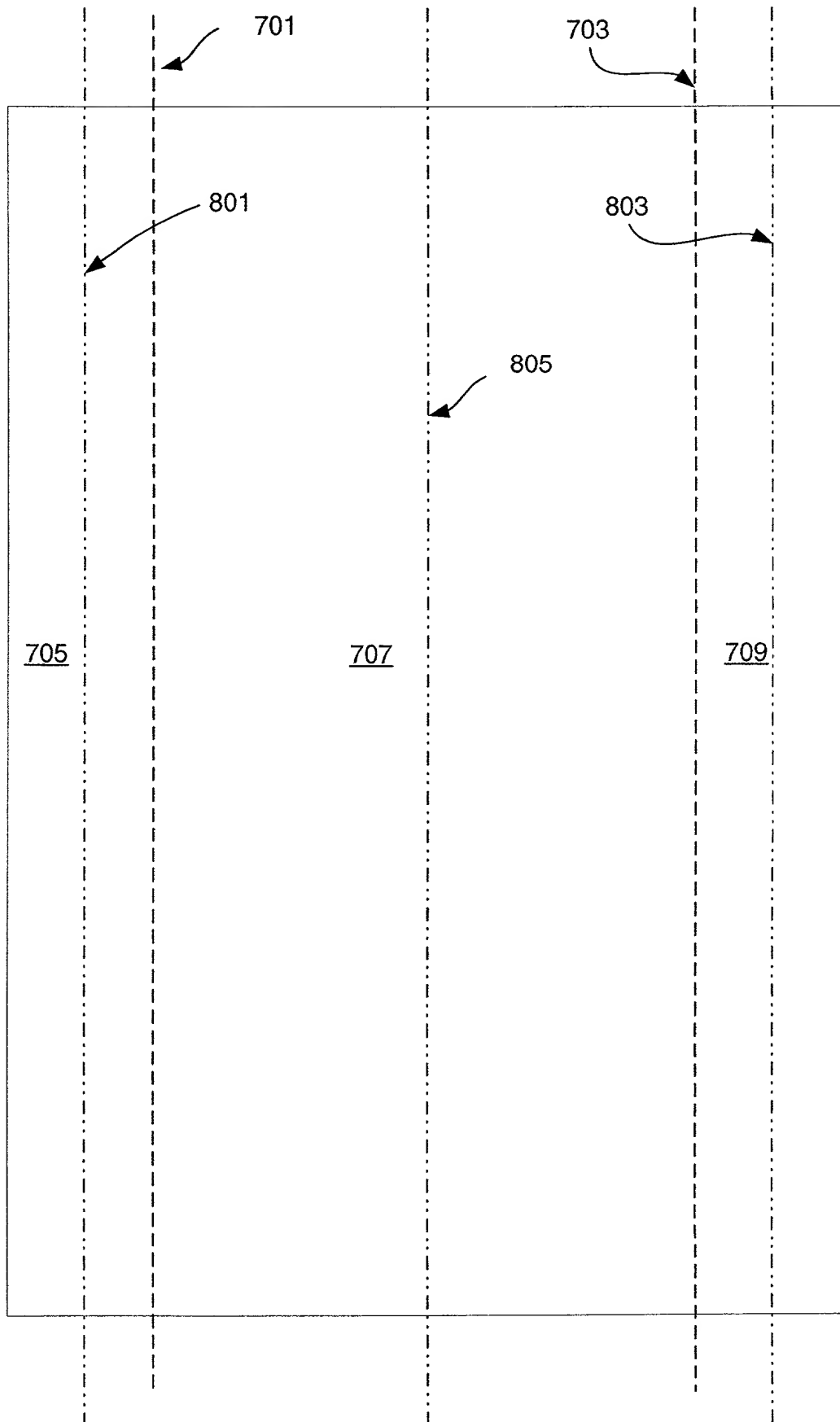


FIG. 8

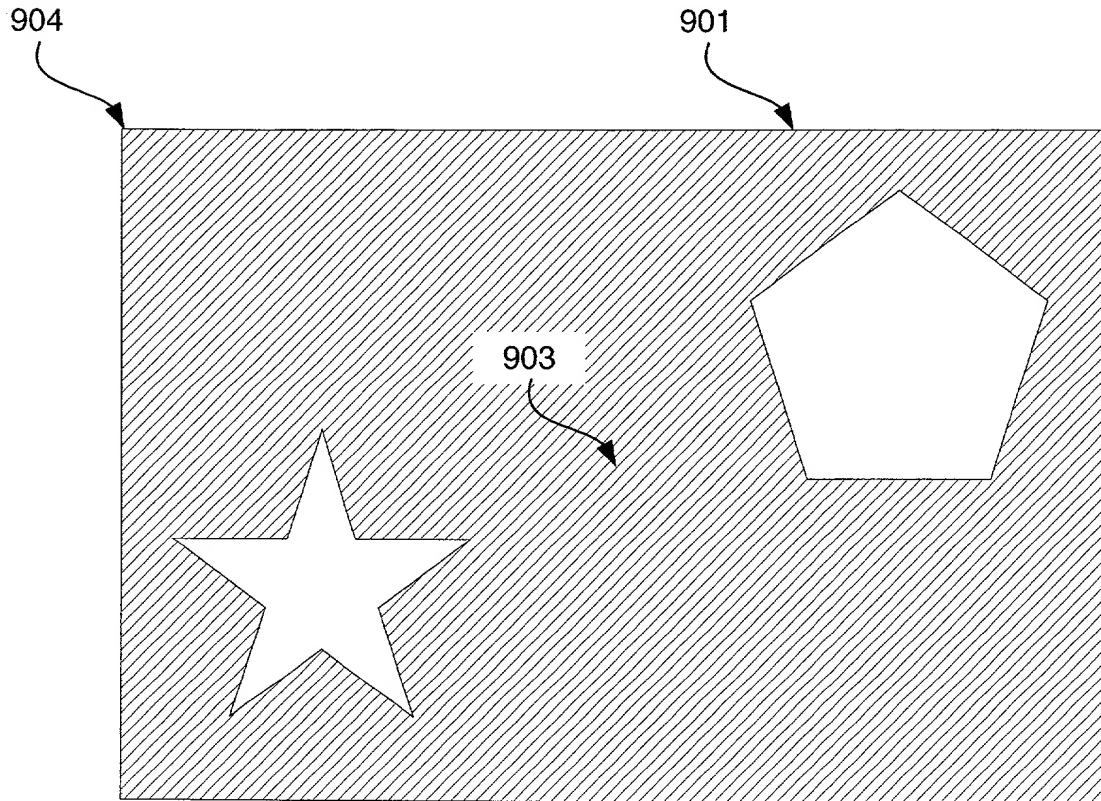


FIG. 9

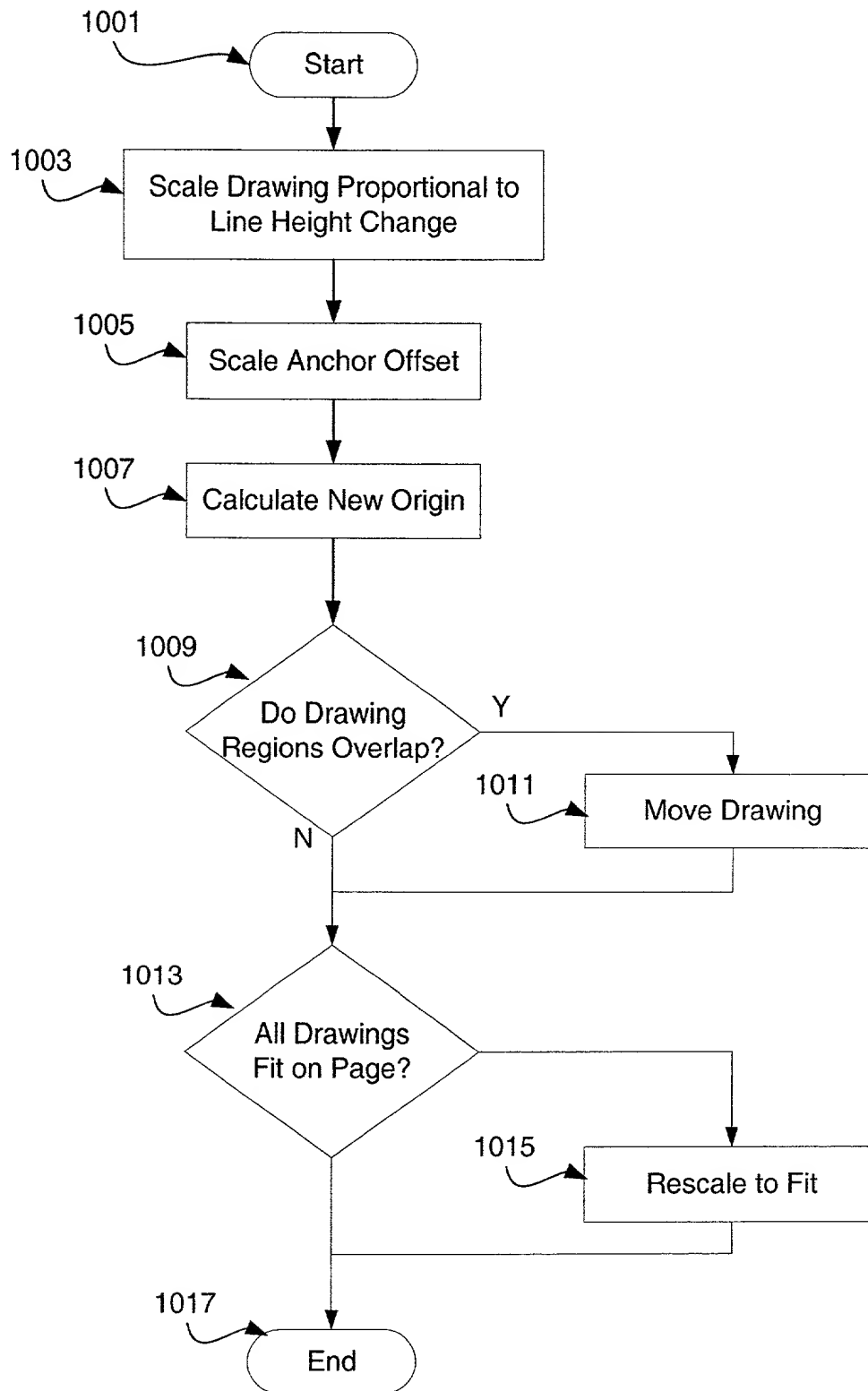


FIG. 10

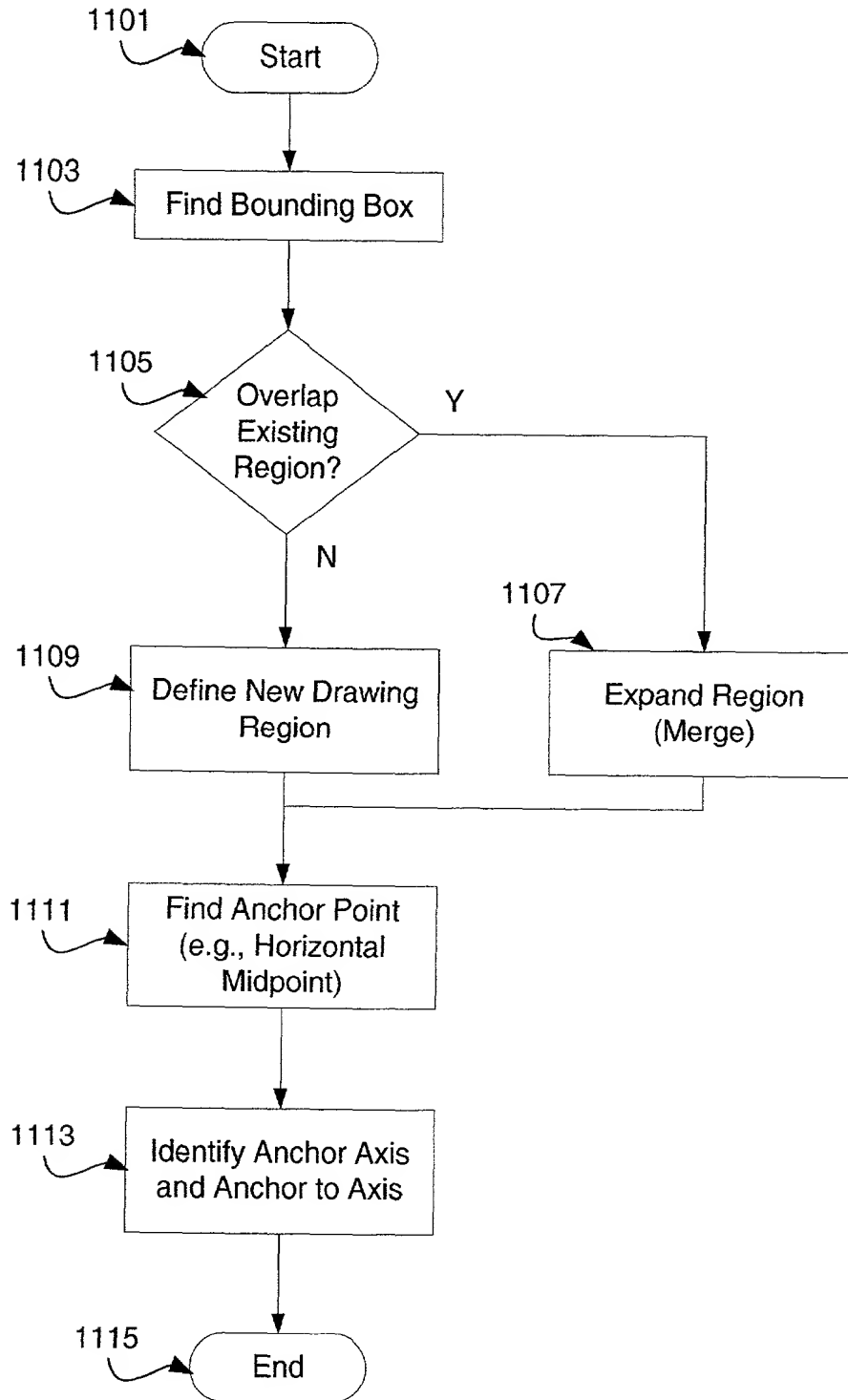


FIG. 11A

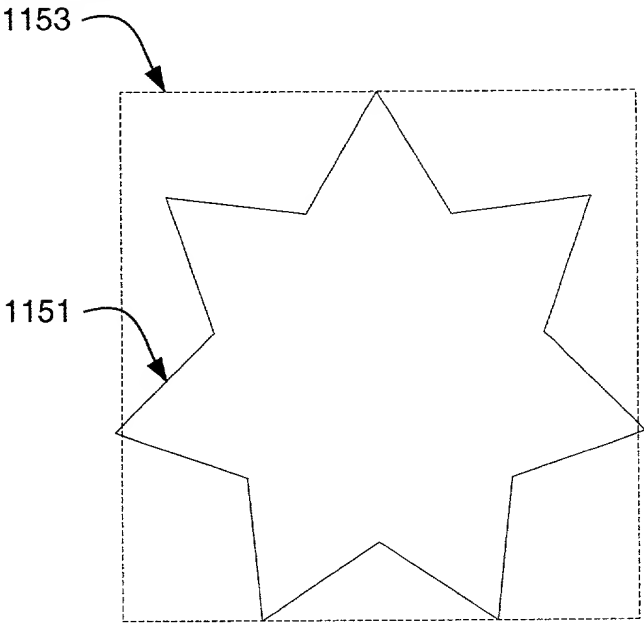


FIG. 11B

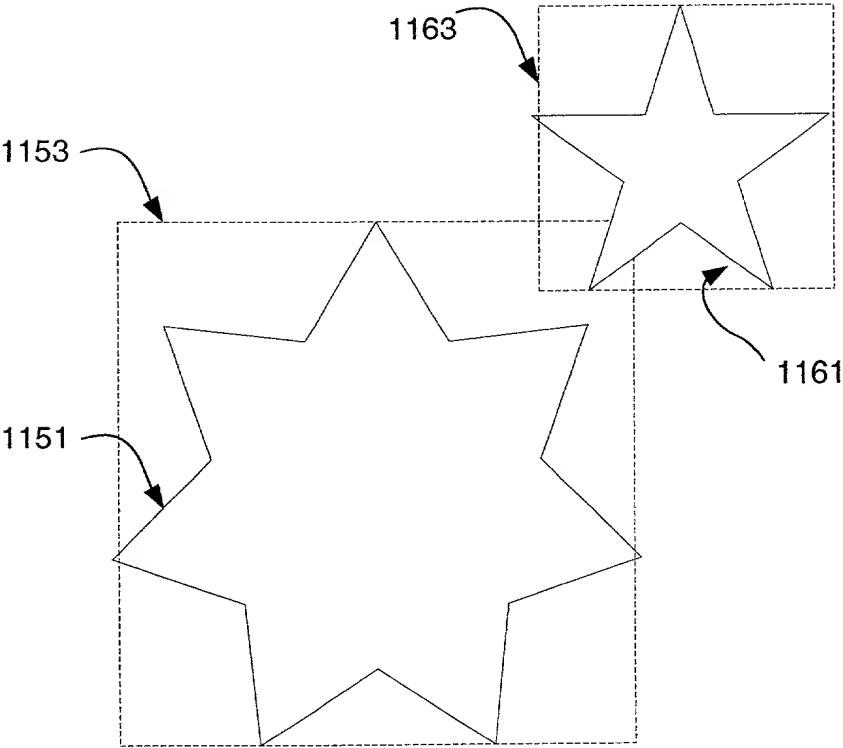


FIG. 11C

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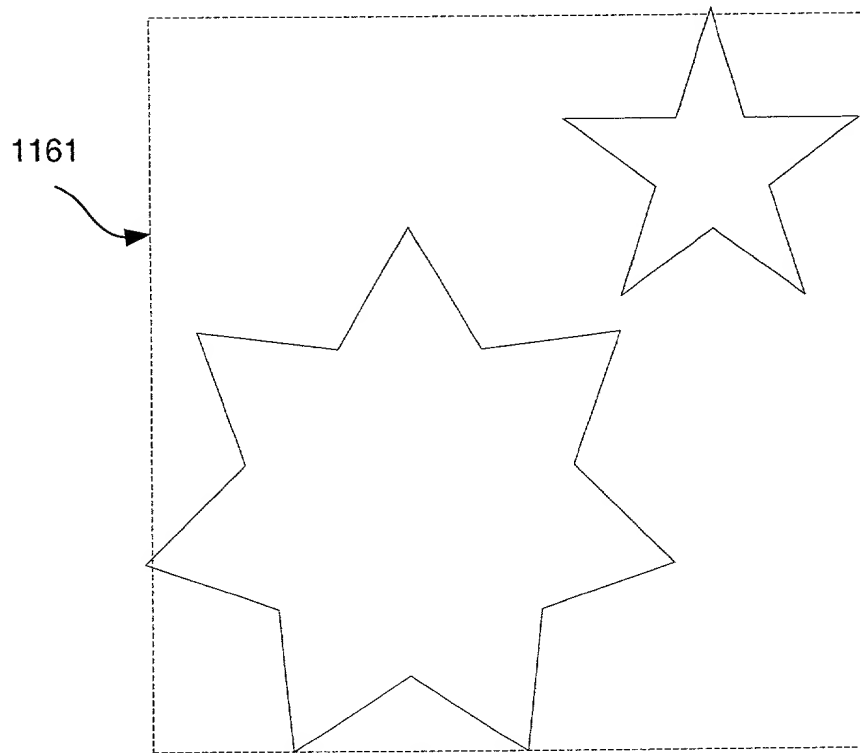


FIG. 11D

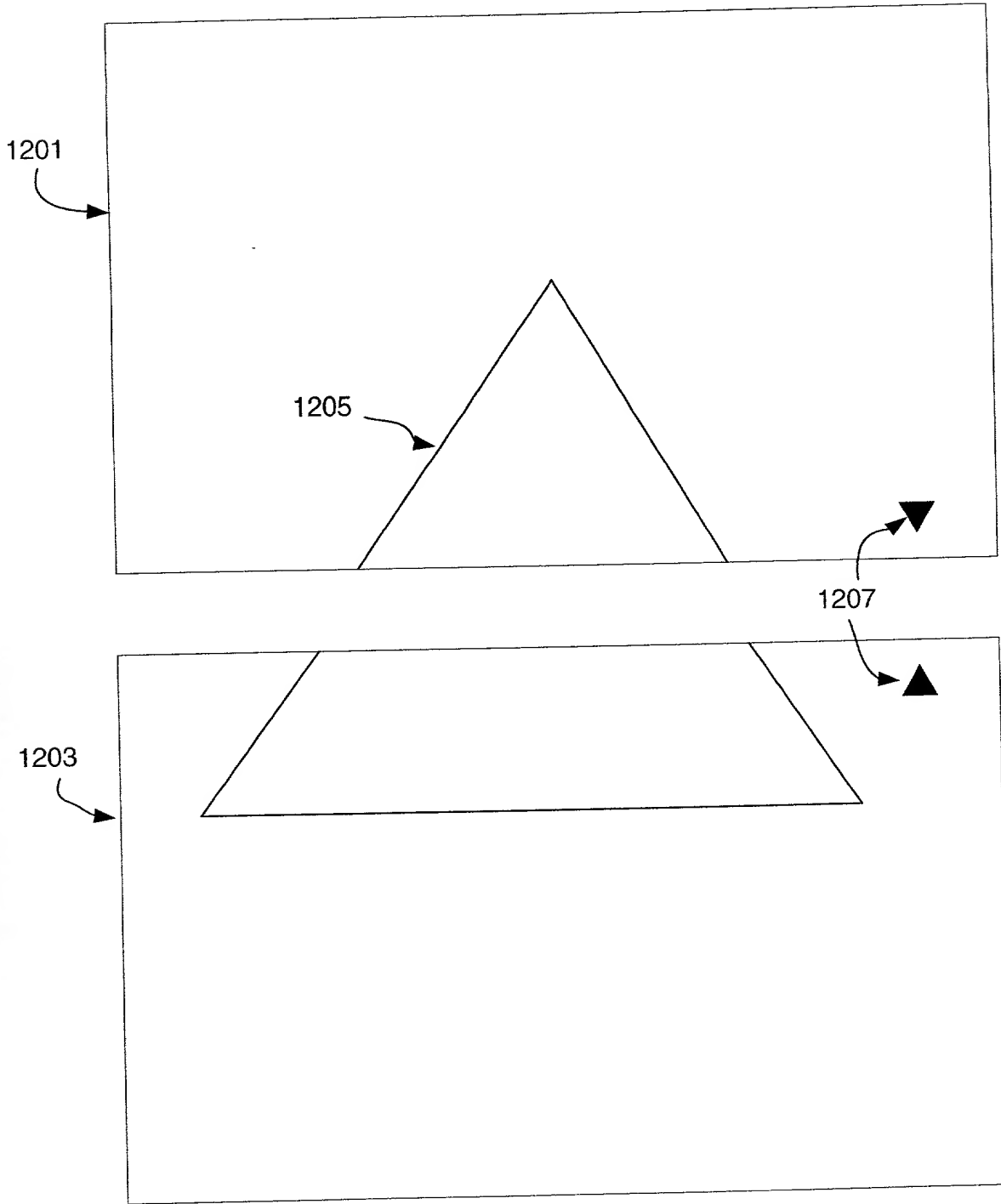


FIG. 12